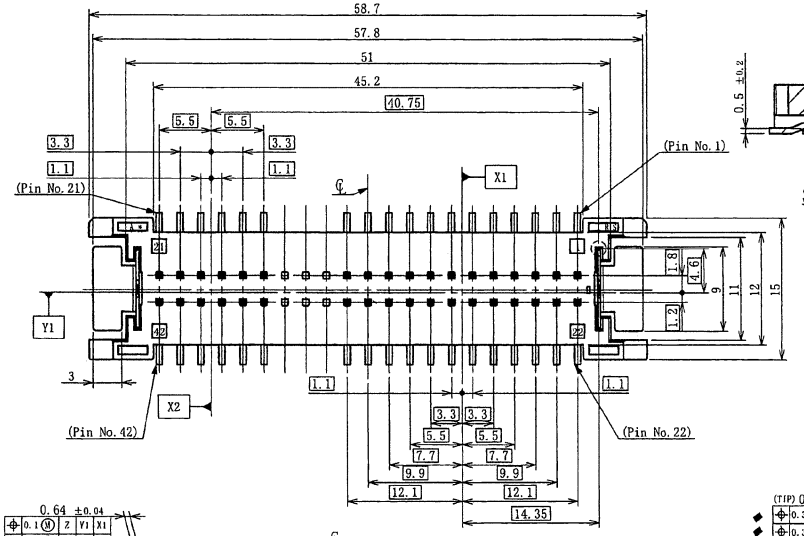
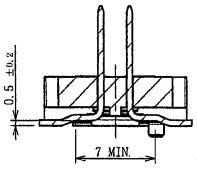
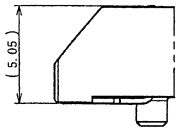




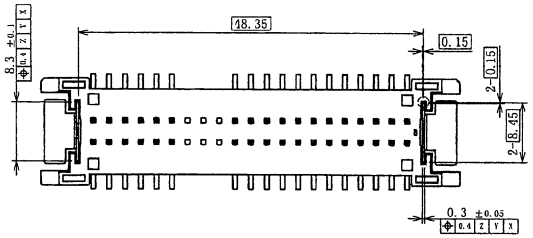
SECT T-T

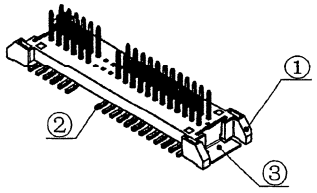


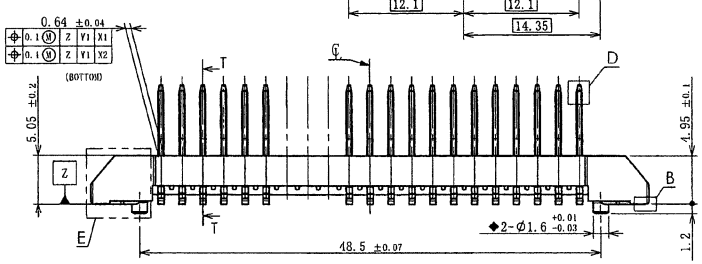
DETAIL E

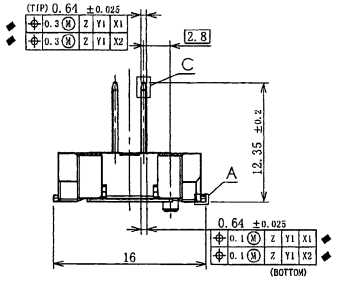
NOTE

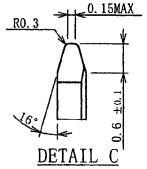
1. コプラナリティ/COPLANARITY : 0.12 max.
2. 本製品は、錫系のめっきを施しておりますので、ウィスカーが発生する可能性がございます。その為、ウィスカー発生に対する保証は困難であり、御社にて御判断の上御使用をお願いします。
THIS PRODUCT UTILIZES LEAD-FREE TIN PLATING. ANY PRODUCT WITH LEAD-FREE TIN PLATING IS SUSCEPTIBLE TO TIN WHISKER. IRISO PROVIDES NO ASSURANCES AGAINST THE GROWTH OF TIN WHISKER EVEN UNDER NORMAL OPERATING CONDITIONS. CUSTOMERS ASSUME ALL RESPONSIBILITY FORANY PRODUCT FAILURES DUE SOLELY TO THE GROWTH OF TIN WHISKERS.
3. ◆特別特性項目：ロット間の工程能力がＣＰＫ=1.33以上満足する事。
◆SPECIAL CHARACTERISTIC : CPK≧1.33



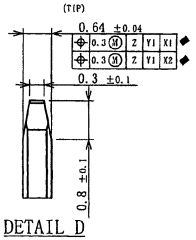




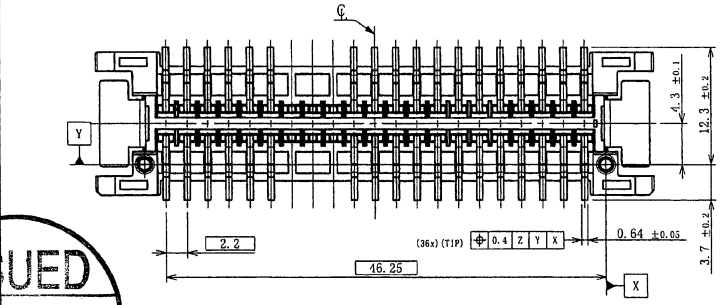


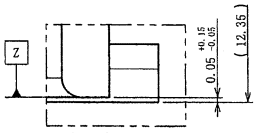


DETAIL C

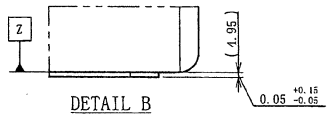


DETAIL D

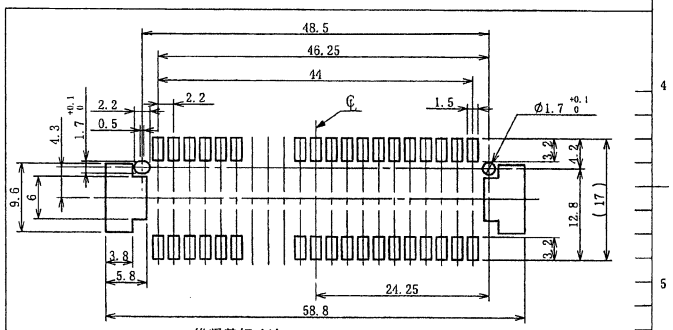




DETAIL A



DETAIL B



推奨基板寸法
一般公差： ±0.05 （マウント面）
RECOMMENDED PCB SIZE
GENERAL TOLERANCE : ±0.05 (MOUNTING SIDE)

ISSUED
15.9.24
IRISO
ENG.DEPT.

LTR 記号	ECN No	REVISION RECORD 記事	DR	CHK	DATE 年月日
1	5059	RELEASED	You	Sheng	'15-09-15
0	--	SKETCHED	You	Sheng	'15-03-25

MATERIAL 材質
①ベース/BASE : LCP樹脂/LCP RESIN, 黒色/BLACK VECTRA E130i
②ポスト/POST : リン青銅/PHOSPHOR BRONZE JIS C5191R-1/2H(t=0.64)
③金具/METAL FLANGE : 黄銅/BRASS(t=0.3±0.05)

FINISH 仕上げ
②、③Ni下地めっき/OVER Ni PLATE : 1.5～3.5μm.
半光沢錫めっき/TIN MATT PLATE : 3.0～6.0μm.

GENERAL TOLERANCES 一般公差
DIM 寸法 ± 0.3
ANGLE 角度 ± 1°

3RD. ANG. PROJ. 三角法

SCALE 尺度 /
UNIT 単位 mm
DR '15-09-15 Yougijun
DSN '10-10-06 H. Yukutake
CHK '15-9-17 Shenghui
APP '15-09-18 zhangjinhui

IRISO ELECTRONICS CO.,LTD
イリソ電子工業株式会社

TITLE 名称
13007シリーズ ピンヘッダー
SERIES 13007 PIN HEADER

EDP No

PARTS No 部番 IMSA-13007B-36Z03-TM1

DWG No 図番 110-413007-033

REV. 1

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